

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Chris J. Newburn, et al.

Application No.: 10/750,715

Filed: December 31, 2003

**For: Processor and Memory Controller Capable
Of Use In Computing System That Employs
Compressed Cache Lines' Worth Of
Information**



Examiner: Hashem Farrokh

Art Unit: 2186

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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AMENDMENT AND RESPONSE

Dear Sir:

This is in response to the Office Action mailed January 4, 2006. Applicant respectfully requests the Examiner to enter the following amendments and consider the following remarks.